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APPLICANT

Marcus BONSE et al.

FILING DATE 03/14/2005

GROUP 3661

LIST OF PRIOR ART CITED BY APPLICANT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
	A01	US 4,715,772	Dec. 29, 1987	Kanayama	414	730	
	A02	US 4,666,303	May 19, 1987	Pryor	356	606	
	A03	US 2006/0137164	Jun. 29, 2006	Kraus	029	407.1	
	A04						

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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO
A05	JP 2-55268	Nov. 26, 1990	JP-Japan			See US 4,715,772	<u> </u>
A06	EP 470939 A1	Feb. 12, 1992	EP-Europe			In English	<u> </u>
A07	DE 19930087 A1	Jan. 11, 2001	DE-Germany			See English Abstract and Office Action of US Patent Application 10/527,723	
A08	JP 04-29382	Mar. 9, 1992	JP-Japan			See English translation	
A09	JP 02-110489	Sep. 4, 1990	JP-Japan			See English translation	
A10	JP 05-147457 A	Jun. 15, 1993	JP-Japan			See English Abstract	
A11	JP-62-106503 A	May 18, 1987	JP-Japan			See English Abstract	

	OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)
A	STMicroelectronics Application Note 667, "Painless microcontroller code by graphical application description," http://www.st.com/stonline/books/pdf/docs/2528.pdf , p. 9, 14 pages
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A.	NIST Manufacturing Engineering Laboratory, "ISD's Research Areas: Intelligent Open Architecture Control of Manufacturing Systems", http://www.isd.mel.nist.gov/research_areas/open_architecture/index.htm (1 of 6) 10/27/2008 12:55:22 PM, Date: 1/25/2001
A.	Office Action of US Patent Application 10/527,723 of November 14, 2008, 14 pages
A.	6 English Translation of JP 04-29382, 14 pages
A.	7 English Translation of JP 02-110489, 14 pages

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.